

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Giovanni TRAVERSO, et al.

Attorney Docket Q67311

Appln. No.: Unknown

Confirmation No.: Unknown

Group Art Unit: Unknown

Filed: November 16, 2001

Examiner: Unknown

For: IMPROVED INTERFACE SYSTEM FOR SYNCHRONOUS HIERARCHY
TELECOMMUNICATIONS NETWORKS

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

The specification is changed as follows:

Page 3, please amend the paragraphs section under the section "**BRIEF DESCRIPTION
OF THE DRAWINGS**" as follows:

In the drawings:

Fig. 1 shows a basic block diagram of an improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention;

Fig. 2 shows a detailed diagram of a first part of the improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention;

Fig. 3 is a Table 3 for illustrating the format of the data frame TRM1 according to the invention; and;

Fig. 4 shows a detailed diagram of a second part of the improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention.

Page 6, amend the sixth full paragraph as follows:

In Fig. 4 shows the board CB1 of the switch matrix CM.

IN THE DRAWINGS:


Attached please find *Proposed Corrections to Formal Drawings* relabeling Figure 3 to Figure 4 and Table 3 to Figure 3.

Preliminary Amendment
Attorney Docket Q67311

REMARKS

Entry and consideration of this Amendment is respectfully requested.

Respectfully submitted,



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Date: November 16, 2001

APPENDIX

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE SPECIFICATION:

The specification is changed as follows:

Page 3, please amend the paragraphs section under the section “**BRIEF DESCRIPTION OF THE DRAWINGS**” as follows:

In the drawings:

- Fig. 1 shows a basic block diagram of an improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention;
- Fig. 2 shows a detailed diagram of a first part of the improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention; ~~and~~
- Fig. 3 is a Table 3 for illustrating the format of the data frame TRM1 according to the invention; and;
- Fig. ~~34~~ shows a detailed diagram of a second part of the improved interfacing system for synchronous hierarchy telecommunication networks, according to the present invention.

Page 6, amend the sixth full paragraph as follows:

In Fig. ~~34~~ shows the board CB1 of the switch matrix CM.

Page 8, amend the fourth paragraph (after line 10) as follows:

In this frame TRM1, the bytes indicated in Table 3, shown in Fig. 3 have the following meanings:

Preliminary Amendment
Attorney Docket Q67311

IN THE DRAWINGS:

Attached please find *Proposed Corrections to Formal Drawings* relabeling Figure 3 to Figure 4 and Table 3 to Figure 3.

3/4 4/4

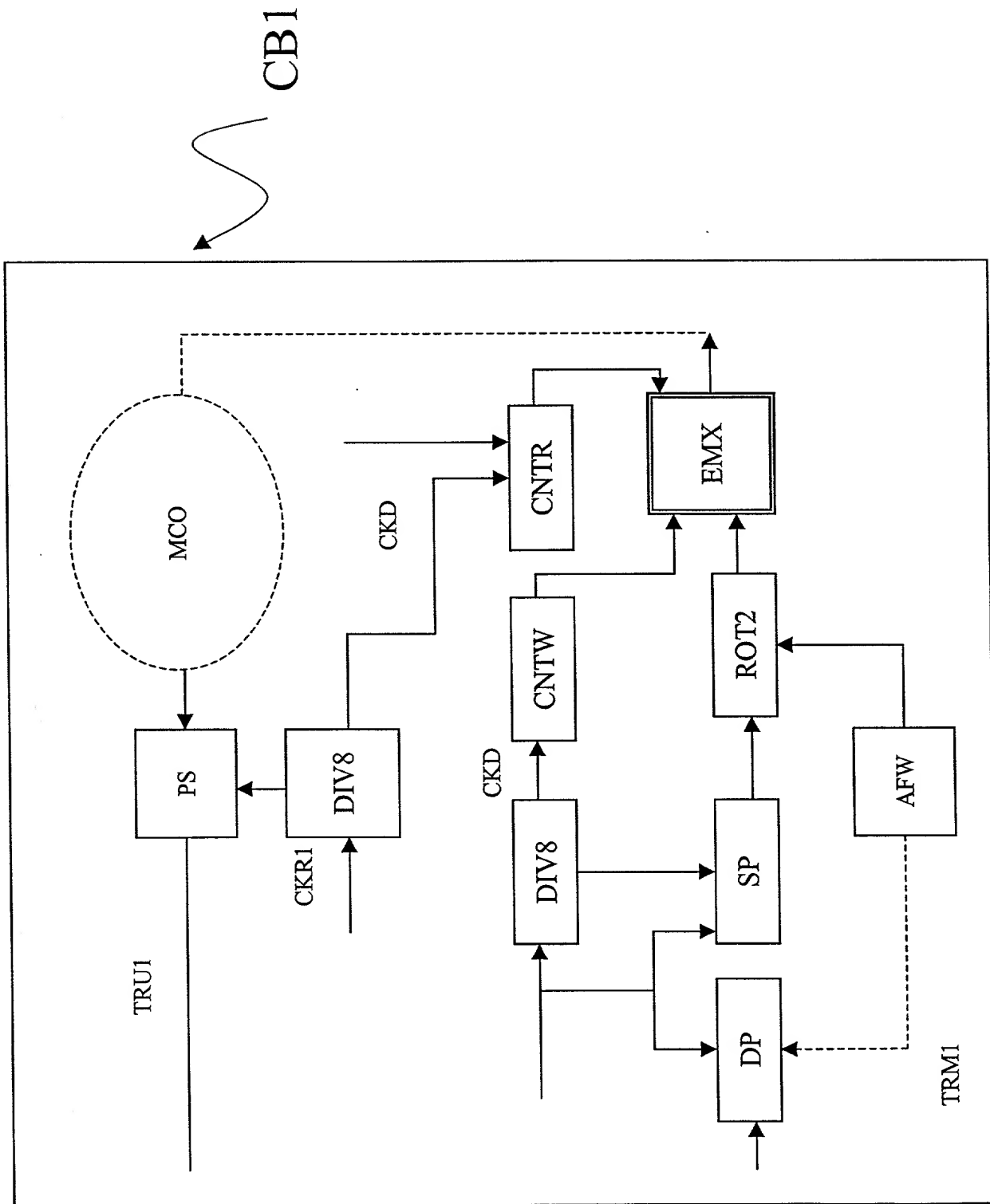


FIG. 3 Fig. 4

[illegible]

~~Table 3~~